## Reg. No:

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## SIDDHARTH INSTITUTE OF ENGINEERING \& TECHNOLOGY:: PUTTUR (AUTONOMOUS)

B.Tech II Year I Semester Regular \& Supplementary Examinations March-2023 ANALOG ELECTRONIC CIRCUITS
(Electrical and Electronics Engineering)
Time: 3 hours
(Answer all Five Units $5 \times 12=60$ Marks)
UNIT-I
1 a Define feedback and illustrate the basic concept of Feedback with suitable block diagram.

b A voltage series negative feedback amplifier has a voltage gain without
feedback of $\mathrm{A}=500$, input resistance $\mathrm{Ri}=3 \mathrm{k} \Omega$, output resistance $\mathrm{Ro}=20 \mathrm{k} \Omega$
and feedback ratio $\beta=0.01$. Calculate the voltage gain Af , input resistance and
output resistance of the amplifier with feedback.

## OR

 CO1 L1 6M
CO3
L3 6 M

## UNIT-II

> 3 a Define Oscillator and explain its principle of operation.
> b In a transistorized Hartley, oscillator the two inductances are 2 mH and $20 \mu \mathrm{H}$. While the frequency is to be changed from 950 kHz to 2050 kHz . Calculate the range over which the capacitor is to be varied.
CO1 L2 6M

> OR
4 a Determine the condition for sustained oscillations for an RC phase shift Oscillator with necessary circuit diagrams.

b Draw the circuit diagram of Colpitts oscillator using BJT and derive the
expression for frequency of oscillations

## UNIT-III

5 a Explain the basic information and pin configuration of an op-amp.
CO1 L2 6M
b Derive the expression for gain of Differential amplifier with two op-amps.
CO5 L2 6M

## OR

6 a What is voltage follower? What are its features and applications?
CO1 L1 6M
b Draw and explain frequency response of practical op-amp.
L2 6M

## UNIT-IV

7 a Design and explain the operation of inverting summing amplifier. ..... CO3 ..... L3 6M
b Draw the input-output waveforms and frequency response of integrator ..... CO1 ..... L1 6M
OR
8 a Draw an op-amp circuit whose output is $\mathrm{Vo}=(\mathrm{V} 3+\mathrm{V} 4)-(\mathrm{V} 1+\mathrm{V} 2)$. ..... CO1 ..... L3 6M
b Derive the equation for frequency of oscillation of astable multivibrator ..... CO4 ..... L3 6M using op-amp.
UNIT-V
9 a Define active filter and give its characteristics. ..... CO 2 ..... L4 6M
b The basic step of a 9 bit DAC is 10.3 mV . If " 000000000 " represents 0 V . ..... CO4 ..... L1 6MWhat output is produced if the input is " 101101111 "?
OR
10 a Explain the first order high pass butter worth filter with a neat circuit ..... CO 2 ..... L2 ..... 6M diagram.
b Design an inverted R-2R ladder DAC for digital input word 001. ..... CO2 ..... L2 ..... 6M

